

Ser. No. 10/519,028
Amdt. dated November 14, 2008
Reply to Office action of June 30, 2008

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Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims

1. (currently amended) A method for managing memory in a video signal processing device comprising:
processing a video signal using a first bank of video filter coefficients;
detecting a change in a video display format of said video signal;
writing video filter coefficient data corresponding to a second bank of video filter coefficients to a second memory in response to said change in said video display format;
disabling a said first memory and a said second memory;
switching an output from said first memory to said second memory in response to a portion of a video signal; and said writing step and a vertical blanking interval;
enabling said first memory and said second memory; and
processing said video signal using said second bank of video filter coefficients.
2. (canceled)
3. (currently amended) The method for managing memory of claim [[2]]1, wherein said video blanking interval is a vertical video blanking interval.
4. (original) The method for managing memory of claim 1 wherein said output is connected to a video filter.
5. (original) The method for managing memory of claim 4 wherein said first memory and said second memory store video filter coefficient data.
6. (original) The method for managing memory of claim 5 wherein said video filter coefficient data is the memory address data of video filter coefficients.

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7. (original) The method for managing memory of claim 1 wherein disabling said first memory and said second memory comprises the steps of disabling the read and write functions of said first memory and said second memory.

8. (currently amended) A method for changing video filter coefficients in a video signal processing device comprising:
detecting a change in a video display format of a video signal;
writing at least one address of a bank of video filter coefficients to a first memory;
disabling said first memory;
switching an output of a second memory to said first memory in response to a ~~portion of a video signal~~ video blanking interval; and
enabling said first memory.

9. (canceled)

10. (currently amended) The method for changing video filter coefficients in a video signal processing device of claim [[9]]8, wherein said video blanking interval is a vertical video blanking interval.

11. (currently amended) The method for changing video filter coefficients in a video signal processing device of claim ~~4~~8 wherein said output is connected to a video filter.

12. (currently amended) The method for changing video filter coefficients in a video signal processing device of claim [[4]]11 wherein said first memory and said second memory store video filter coefficient data.

13. (currently amended) An apparatus for selecting one of a plurality of video filter coefficients comprising:
a first memory for storing a first set of video filter data corresponding to a first bank of video filter coefficients;
a second memory for storing a second set of video filter data corresponding to a second bank of coefficients;

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a processor for processing a video signal using said first set of video filter data, said processor further operative to detect a video display format of said video signal, for writing video filter data to said second memory in response to said change in said video display format, and for processing said video signal using said second set of video filter data;

a switch (422) for selecting either said first memory or said second memory and coupling said selected memory to said processor; and

a bank switching device for detecting said processor writing video filter coefficient data, detecting a portion- video blanking interval of a said video signal and changing the state of said switch in response to detecting said writing of video coefficient data followed by said detecting of said video blanking interval.

14. (canceled)

15. (currently amended) The apparatus of claim [[14]]13, wherein said video blanking interval is a vertical video blanking interval.

16. (original) The apparatus of claim 13 wherein said first set of video filter data and said second set of video filter data are a plurality of memory address locations of video filter coefficients.

17. (original) The apparatus of claim 13 wherein said first set of video filter data and said second set of video filter data are a plurality of video filter coefficients.

18. (original) The apparatus of claim 13 wherein said switch is a multiplexer.

19. (original) The apparatus of claim 13 wherein said apparatus is included within an integrated circuit.